

Appln. No. 10/627,976 Annotated Marked-Up Drawing



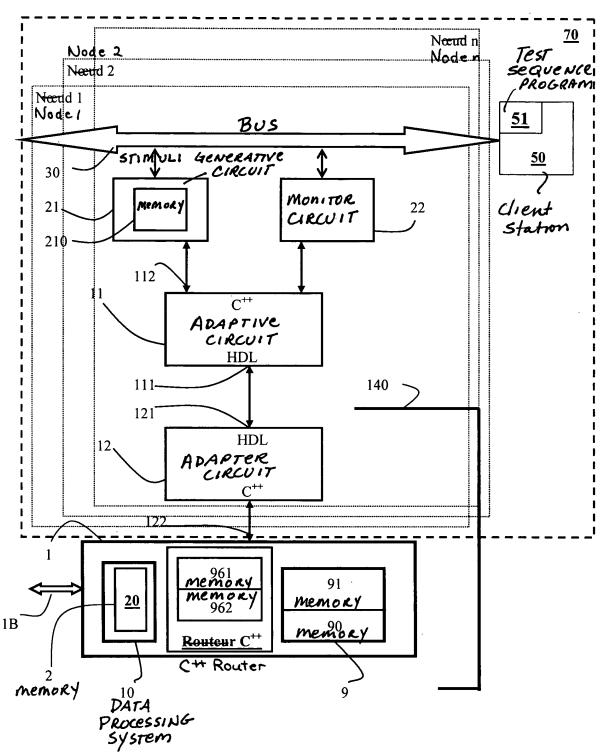


FIGURE 1



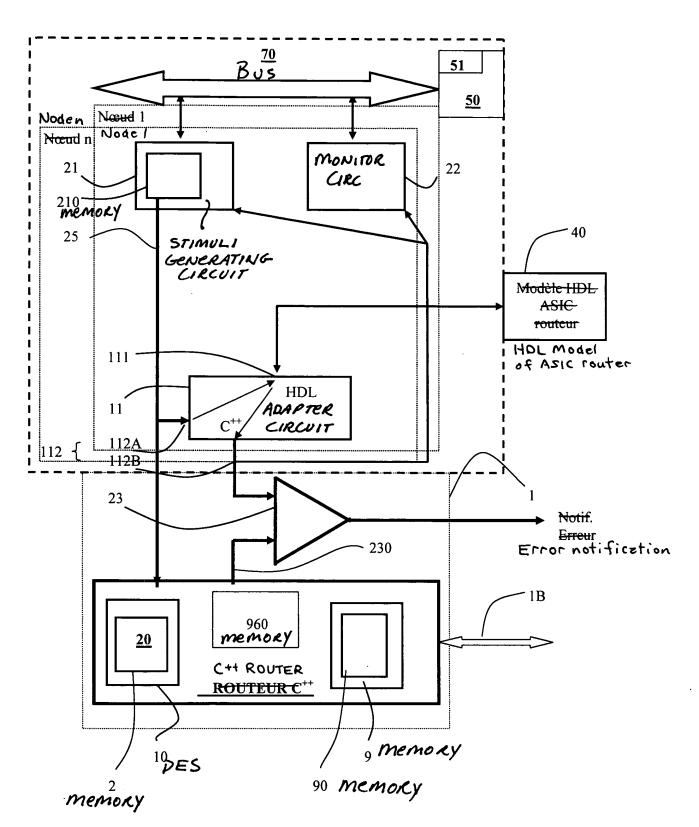


FIGURE 2



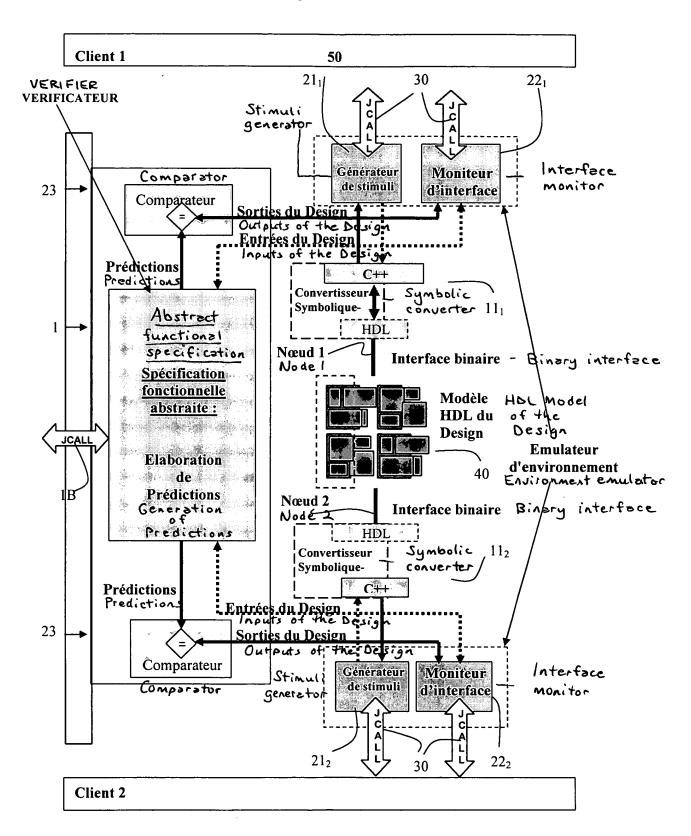


Figure 3: Mode "Vérification"

~ Verification " Mode



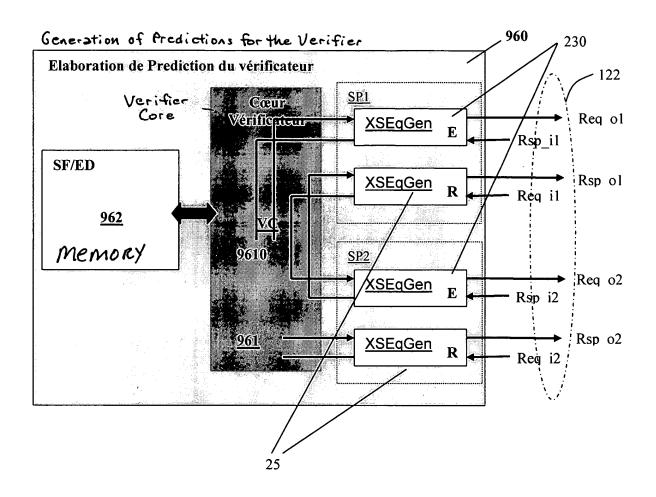


Figure 4: Architecture interne Vérificateur

Internal Architecture of the Verifier